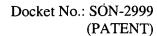




AMENDMENT TRANSMITTAL LETTER					Docket No. SON-2999
Application No.		Filing Date		Examiner	Art Unit
10/827,351 Conf. No. 2615		April 20, 2004		H.N. Nguyer	n 2824
plicant(s): Kou	Nagata				
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1233 20th Stree Suite 501	et, N.W.				
Washington, DC					
(202) 955-3750					
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Kou Nagata

Application No.: 10/827,351

Confirmation No.: 2615

Filed: April 20, 2004

Art Unit: 2824

For: SEMICONDUCTOR MEMORY APPARATUS

AND SELF-REPAIR METHOD

Examiner: H. N. Nguyen

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated March 30, 2005 (Paper No./Mail Date 20050318), please amend the above-identified U.S. patent application as follows: